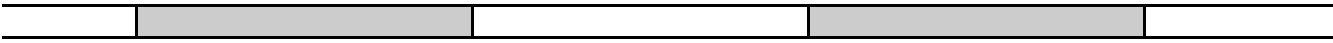


Entrées

AEN

A digital signal line for AEN. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low.

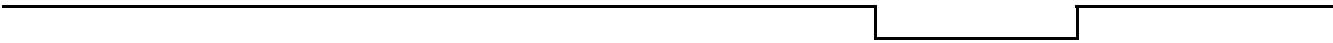
ADR

A digital signal line for ADR. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low. The high periods are shaded gray.

MEMR

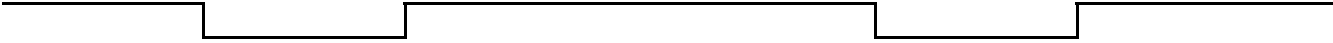
A digital signal line for MEMR. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low.

MEMW

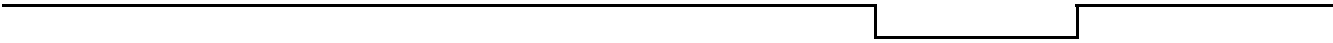
A digital signal line for MEMW. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low.

Sorties

CIK

A digital signal line for CIK. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low.

RW

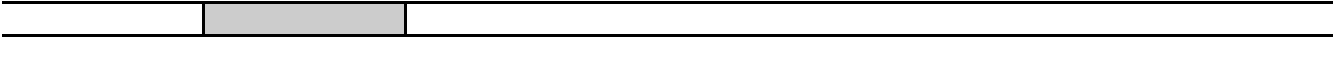
A digital signal line for RW. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low.

CS

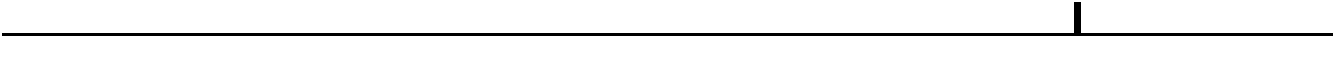
A digital signal line for CS. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low.

Actions

Le registre place la donnée sur le bus

A digital signal line for the action 'Le registre place la donnée sur le bus'. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low. The high periods are shaded gray.

Le registre mémorise la donnée

A digital signal line for the action 'Le registre mémorise la donnée'. It starts at a low level, transitions to high, then back to low, then high again, and finally back to low. The high periods are shaded gray.